

HC971 U.S. PRO
09/920180
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ISSUE CLASSIFICATION	
Class	Subclass

O.I.P.E.

PATENT DATE

SCANNED TK2 Q.A. Edi

APPLICATION NO. 09/920180	CONT/PRIOR D	CLASS 713	SUBCLASS 300	ART UNIT 2181	EXAMINER <i>Amie</i>
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Gary Swoboda

2115

Chandrasekhar

Apparatus and method for bus power measurement in a digital signal processor

PTO-2040
12/99

ORIGINAL

CROSS REFERENCE(S)

CLASS

SUBCLASS

CLASS

SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS			SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE <div style="display: flex; justify-content: space-between;"> Amount Due Date Paid </div>	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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